

## APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification of Application Serial No. 10/826,713, filed 04/16/04; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:		
Wafer-Level Assen	ably Method for Chip-Size Devices Ha	wing Flipped Chips
	NT PRACTITIONERS AT CUSTOMER NUMBER 2 ALL BUSINESS IN THE PATENT AND TRADEMA	
SEND CORRESPONDENCE TO:		DIRECT TELEPHONE CALLS TO:
Sam Tung Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265		Sam Tung (972) 917-5355
NAME OF INVENTOR: (1)	NAME OF INVENTOR: (2)	NAME OF INVENTOR: (3)
William D. Boyd	Chris Haga	Anthony L. Coyle
RESIDENCE & POST OFFICE ADDRESS:  3553 Nova 1701 Charles Pt. 2020  Y Fisco Planor Texas 75093 750 34	RESIDENCE & POST OFFICE ADDRESS:  2600 Aspen Dr.  McKinney, Texas 75070	RESIDENCE & POST OFFICE ADDRESS: 6616 Pinebluff Dr. Plano, Texas 75074
COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP:
United States	United States	United States
SIGNATURE OF INVENTOR:	signature of inventor:  × Ch. Haza	SIGNATURE OF INVENTOR:
DATE/ * 5/5/04	DATE: x 5/04/04	DATE: x 5/5/04

ATTORNEY'S DOCKET NO. TI-37214

## APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY

TITLE OF INVENTION:			
Wafer-Level Assembly Method for Chip-Size Devices Having Flipped Chips			
NAME OF INVENTOR: (4)	NAME OF INVENTOR: (5)	NAME OF INVENTOR: (6)	
Leland S. Swanson	Quang X. Mai	N/A	
RESIDENCE & POST OFFICE ADDRESS:  4811 Meadow Ridge Circle McKinney, Texas 75070	RESIDENCE & POST OFFICE ADDRESS: 3011 East Hickory Park Circle Sugar Land, Texas 77479	RESIDENCE & POST OFFICE ADDRESS:	
COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP:	
United States	United States		
SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:	
x Leland Levanin	Gwapellan		
DATE:	DATE:	DATE:	
x 04 May 04	05 May ory		